Faculty of Engineering

COURSE OUTLINE

ELEC 569A– Selected Topics in Computer Engineering: Low Power Design

Term – SUMMER 2015 (201505)

Instructor
Dr. Amirali Baniasadi
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Office Hours
Days: Only by appointment.

Course Objectives
- Understanding and analyzing low power system optimizations.

Learning Outcomes
- Learning to Analyze and Design low-power circuits and microarchitectures.

Syllabus
- Low-Power Branch Predictors, Low-Power Execution Units, Low-Power Memory Systems, Low-Power Register Files

A-Section(s): A01 / CRN 30361
Days: Mondays
Time: 14:00-17:50
Location: ECS 130

Required Text
Title: Power Aware Design Methodologies
Author: M. Pedram and J. M. Rabaey
Publisher: Springer
Year:

Optional Text
Title:
Author:
Publisher:
Year:

References:

Assessment:
Project or Presentations 50%  Date: Will be announced in advance
Final 50%
**Note:** (sample notes for the instructors)

Failure to complete all laboratory requirements will result in a grade of N being awarded for the course. Failure to pass the final exam will result in a failing grade for the course.

The final grade obtained from the above marking scheme for the purpose of GPA calculation will be based on the percentage-to-grade point conversion table as listed in the current Undergraduate Calendar.

There will be no supplemental examination for this course.

http://web.uvic.ca/calendar/FACS/UnIn/UARe/Grad.html

**Note to Students:**

Students who have issues with the conduct of the course should discuss them with the instructor first. If these discussions do not resolve the issue, then students should feel free to contact the Chair of the Department by email or the Chair's Secretary to set up an appointment.

Accommodation of Religious Observance
http://web.uvic.ca/calendar/GI/GUPo.html

Policy on Inclusivity and Diversity
http://web.uvic.ca/calendar/GI/GUPo.html

Standards of Professional Behaviour

You are advised to read the Faculty of Engineering document Standards for Professional Behaviour in current Undergraduate Calendar, which contains important information regarding conduct in courses, labs, and in the general use of facilities.

Cheating, plagiarism and other forms of academic fraud are taken very seriously by both the University and the Department. You should consult entry in current Undergraduate Calendar for the UVic policy on academic integrity.

http://www.uvic.ca/engineering/assets/docs/professional-behaviour.pdf

**Course Lecture Notes**

Unless otherwise noted, all course materials supplied to students in this course have been prepared by the instructor and are intended for use in this course only. These materials are NOT to be re-circulated digitally, whether by email or by uploading or copying to websites, or to others not enrolled in this course. Violation of this policy may in some cases constitute a breach of academic integrity as defined in the UVic Calendar.