Instructor: 
Dr. Mihai SIMA  
Phone: (250) 721-8680  
E-mail: msima@ece.uvic.ca

Office Hours:  
Days: Monday  
Time: 13:00-15:00  
Location: EOW 313

Lectures:  
A-Section(s): A01/A02 / CRN 30726/30727  
Days: Monday, Thursday  
Time: 8:30-9:50  
Location: ECS-124

Labs:  
B-Section(s)  
Days  
Time

Location: ELW

Required Text:  
Title: course notes available for download on the course webpage  
Author: Mihai SIMA  
Year: 2012

Optional Text:  
Title: Embedded System Design: A Unified Hardware/Software Introduction  
Author: F. Vahid and T. Givargis  
Publisher: John Wiley & Sons  
Year: 2001

Title: Computers as Components: Principles of Embedded Computing Systems Design  
Author: W. Wolf  
Publisher: Morgan Kaufmann  
Year: 2000

Assessment:  
Project: 50%  
Labs  
Mid-term 20%  
Date: TBD  
Final 30%

Due dates for assignments:  
TBD
The final grade obtained from the above marking scheme will be based on the following percentage-to-grade point conversion:

<table>
<thead>
<tr>
<th>Passing Grades</th>
<th>Grade Point Value</th>
<th>Percentage For Instructor Use Only</th>
</tr>
</thead>
<tbody>
<tr>
<td>A+</td>
<td>9</td>
<td>90 - 100</td>
</tr>
<tr>
<td>A</td>
<td>8</td>
<td>85 - 89</td>
</tr>
<tr>
<td>A-</td>
<td>7</td>
<td>80 - 84</td>
</tr>
<tr>
<td>B+</td>
<td>6</td>
<td>77 - 79</td>
</tr>
<tr>
<td>B</td>
<td>5</td>
<td>73 - 76</td>
</tr>
<tr>
<td>B-</td>
<td>4</td>
<td>70 - 72</td>
</tr>
<tr>
<td>C+</td>
<td>3</td>
<td>65 - 69</td>
</tr>
<tr>
<td>C</td>
<td>2</td>
<td>60 - 64</td>
</tr>
<tr>
<td>D</td>
<td>1</td>
<td>50 - 59</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Failing Grades</th>
<th>Grade Point Value</th>
<th>Percentage For Instructor Use Only</th>
<th>Notes</th>
</tr>
</thead>
<tbody>
<tr>
<td>E</td>
<td>0</td>
<td>35 - 49</td>
<td>Fail, conditional supplemental exam.</td>
</tr>
<tr>
<td>F</td>
<td>0</td>
<td>0 - 34</td>
<td>Fail, no supplemental exam.</td>
</tr>
<tr>
<td>N</td>
<td>0</td>
<td>0 - 49</td>
<td>Did not write examination, Lab or otherwise complete course requirements by the end of the term or session; no supplemental exam.</td>
</tr>
</tbody>
</table>

The rules for supplemental examinations are found on page 80 of the current 2013/14 Undergraduate Calendar.

<table>
<thead>
<tr>
<th>Term in which E Grade was obtained:</th>
<th>Application Deadline for Supplemental Exam</th>
<th>Supplemental Exam Date</th>
</tr>
</thead>
<tbody>
<tr>
<td>First term of Winter Session (Sept – Dec)</td>
<td>February 28 in the following term</td>
<td>First week of following May</td>
</tr>
<tr>
<td>Second term of Winter Session (Jan – Apr)</td>
<td>June 30 in the following term</td>
<td>First week of following September</td>
</tr>
<tr>
<td>Summer Session (May – Aug)</td>
<td>October 31 in the following term</td>
<td>First week of following January</td>
</tr>
</tbody>
</table>

Deferred exams will normally be written at the start of the student's next academic term; i.e., approximately 4 months following the deferral of the exam.

Course Description

1. Course Objectives
   - Expose the students to the embedded systems world
   - Make the students understand how to approach the design of embedded systems
   - Show the students where to look for information and how to interpret it

2. Learning Outcomes
   - Ability to choose an appropriate embedded processor for a specific application
   - Ability to write optimized embedded software
   - Ability to implement embedded applications in fixed-point arithmetic
   - Ability to perform optimal hardware-software co-design
3. Syllabus

Note to Students:
Students who have issues with the conduct of the course should discuss them with the instructor first. If these discussions do not resolve the issue, then students should feel free to contact the ECE Chair by email or the ECE Chair's secretary to set up an appointment.

Accommodation of Religious Observances
See http://web.uvic.ca/calendar2014/GI/GUPo.html

Policy on Inclusivity and Diversity
See http://web.uvic.ca/calendar2014/GI/GUPo.html

Standards of Professional Behaviour
You are advised to read the Faculty of Engineering document Standards for Professional Behaviour at http://www.uvic.ca/engineering/current/undergrad/index.php#section0-25 which contains important information regarding conduct in courses, labs, and in the general use of facilities.

Cheating, plagiarism and other forms of academic fraud are taken very seriously by both the University and the Department. You should consult http://web.uvic.ca/calendar2014/FACS/UnIn/UARe/PoAcI.html for the UVic policy on academic integrity.

Plagiarism detection software may be used to aid the instructor and/or TA's in the review and grading of some or all of the work you submit.