CENG 440 ASSIGNMENT 3

1. Problem 9.4 of text.

2. Problem 9.5 of text.

3. Problem 9.7 of text.

4. Problem 9.13 of text

5. Use the state diagram of Figure Ex9.16 on page 493 of text
   a. Design a circuit using JK flip-flops and the tabular method.
   b. Design a circuit using JK flip-flops by writing the equations directly from the diagram by considering asserted next state variables.
   c. Design a circuit using JK flip-flops by writing the equations directly from the diagram by considering state variable transitions.