1. With a PC based logic analyzer such as the Borge Logic Analyzer what information must be sent from the PC to the logic analyzer pod?

2. Do a paper and pencil Booth Multiply of 11011 times 01101 that clearly shows the register contents after each step.
3. How many 4-bit carry lookahead generators are required for a 128-bit carry lookahead adder? How many gate delays are required for such an adder?

4. Demonstrate nonrestoring division by dividing 1011 by 0100. Show the register content after each step.
5. A 4-bit multiplier can be implemented using 2-bit multipliers and some adders. A 2-bit multiplier can be implemented using 1-bit multipliers and some adders. Give a circuit for a 4-bit multiplier that uses this hierarchical approach.