

Design of Substrate Integrated Waveguide Components Using Mode-Matching Techniques

Zamzam Kordiboroujeni and Jens Bornemann

Department of Electrical and Computer Engineering, University of Victoria, Victoria, BC, V8W 2Y2
Canada

Abstract — A simple, yet efficient mode-matching technique (MMT) is presented for the analysis and design of substrate integrated waveguide (SIW) circuits. The computational domain is formed by a rectangular box of electric and magnetic walls within which square via holes can be arbitrarily moved during an optimization process. The small substrate height limits the modes to those of the TE_{m0} -mode set. In order to adapt to common fabrication facilities, in which circular via holes rather than square ones are used, a simple conversion formula is introduced. Two examples of quasi-elliptic filters are presented. The excellent agreement of responses from the MMT with square vias and CST with circular vias validates the analysis and design procedure.

Index Terms — Mode-matching techniques, substrate integrated waveguide, computer-aided design.

I. INTRODUCTION

Over the last decade, substrate integrated waveguide (SIW) circuitry has evolved into a mature technology that is increasingly used by industry in commercial applications. In most cases, however, SIW components are designed using commercial full-wave solvers that require extensive computation times if optimization involves the iterative movement of a large number of via holes on the board.

Therefore, it is not surprising that other attempts have been made to accelerate the analysis and design process of SIW components. In order to determine the propagation characteristics, Floquet's theorem is usually used in combination with a numerical technique, e.g. [1], and parameter extraction is performed with the domain-decomposition FDTD [2]. Passive SIW components are analyzed with a variety of methods including – to name only a few – the finite-element time-domain (FETD) technique [3], the method of moments (MoM) [4, 5], the boundary integral-resonant mode expansion (BI-RME) method [6, 7], and combinations of the mode-matching technique (MMT) with the MoM [8, 9] or the spectral-domain approach (SDA) [10].

This paper adds to numerical techniques for SIW circuits an efficient MMT procedure that is based on via holes with initially square cross section. That makes the method fast and flexible. Then, in order to comply with standard circular-via PCB fabrication techniques, a conversion from square to circular via holes is performed.

II. THEORY

Fig. 1 shows the top view of an SIW structure with square vias. The computational domain is limited by top and bottom electric as well as left and right (not shown) magnetic walls. Input ports are selected as dielectric-filled waveguides whose widths correspond to the equivalent widths of the SIW closest to the port [11]. In order to set up an MMT routine, we have to distinguish between three different scenarios as pointed out in Fig. 1 at locations A, B and C.

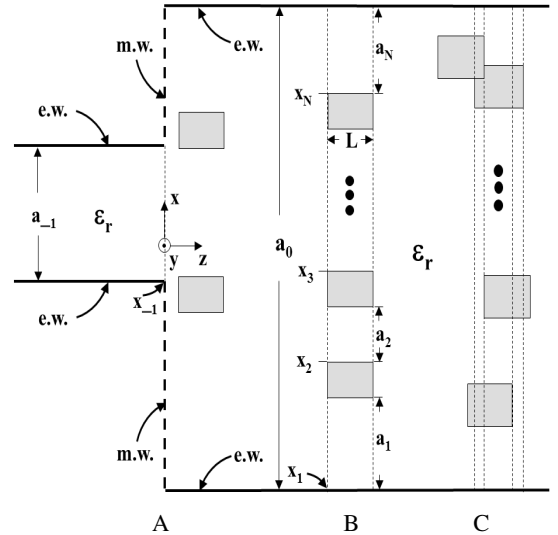


Fig. 1. Top view of SIW structure with square vias.

Using the set of TE_{m0} modes, the vector potential in each region of Fig. 1 can be expressed as:

$$A_{hz}^v = \sum_m \sqrt{Z_{hm}^v} \frac{a_v}{m\pi} \underbrace{\sqrt{\frac{2}{a_v b}} \cos\left\{\frac{m\pi}{a_v}(x - x_v)\right\}}_{T_{hm}^v(x)} \cdot \left[F_m^v \exp(-jk_{zm}^v z) + B_m^v \exp(+jk_{zm}^v z) \right] \quad (1)$$

from which the transverse field components are derived.

$$E_y^v = \frac{\partial A_{hz}^v}{\partial x}, \quad H_x^v = \frac{-1}{j\omega\mu} \frac{\partial^2 A_{hz}^v}{\partial x \partial z} \quad (2)$$

In (1) and (2), v denotes the region, m is the mode number, F and B are forward and backward traveling wave amplitudes,

respectively, k_z is the propagation constant, and the impedance is defines as $Z_n = \omega \mu / k_z$.

At input (or output) ports, we have to solve a discontinuity between two dielectric filled waveguides at a magnetic wall interface (Fig. 1, location A). (Note that the magnetic wall interface is chosen initially due to the fact that in a real SIW, there would be an open substrate with microstrip ports.) Let index m denote the modes in region $v = -1$ and index k the ones in region $v = 0$, then after multiplying with the respective orthogonal mode functions [12], we obtain the following coupling elements

$$J_{m,k} = \frac{2}{\sqrt{a_{-1}a_0}} \int_{x_{-1}}^{x_{-1}+a_{-1}} \sin\left\{\frac{m\pi}{a_{-1}}(x-x_{-1})\right\} \cdot \sin\left\{\frac{k\pi}{a_0}\left(x+\frac{a_0}{2}\right)\right\} dx \quad (3)$$

where the origin in x direction is assumed at the center of the substrate (Fig. 1). After combining matrix J with the frequency-dependent impedance terms:

$$\mathbf{M} = \text{Diag}\left\{\sqrt{Y_{hm}^{-1}}\right\}(\mathbf{J})\text{Diag}\left\{\sqrt{Z_{hk}^0}\right\} \quad (4)$$

the modal scattering matrix of the discontinuity is obtained as

$$\begin{aligned} \mathbf{S}_{11} &= [\mathbf{M}\mathbf{M}^T + \mathbf{U}]^{-1} [\mathbf{M}\mathbf{M}^T - \mathbf{U}] \\ \mathbf{S}_{12} &= 2[\mathbf{M}\mathbf{M}^T + \mathbf{U}]^{-1} \mathbf{M} = \mathbf{S}_{21}^T \\ \mathbf{S}_{21} &= \mathbf{M}^T [\mathbf{U} - \mathbf{S}_{11}] = \mathbf{S}_{12}^T \\ \mathbf{S}_{22} &= \mathbf{U} - \mathbf{M}^T \mathbf{S}_{12} \end{aligned} \quad (5)$$

where \mathbf{U} denotes the unit matrix.

The discontinuity between a dielectric waveguide and a column of square via holes (Fig. 1, location B) is treated similarly, except for the fact that an N -furcated waveguide is obtained if the section contains $N-1$ via holes. Thus N different coupling integrals are obtained

$$J_{mk}^n = \frac{2}{\sqrt{a_0a_n}} \int_{x_n}^{x_n+a_n} \sin\left\{\frac{m\pi}{a_0}\left(x+\frac{a_0}{2}\right)\right\} \cdot \sin\left\{\frac{k\pi}{a_n}(x-x_n)\right\} dx \quad (6)$$

with their frequency-depending extensions:

$$\mathbf{M}^n = \text{Diag}\left\{\sqrt{Y_{hm}^0}\right\}(\mathbf{J})\text{Diag}\left\{\sqrt{Z_{hk}^n}\right\}. \quad (7)$$

By combining all matrices \mathbf{M}^n in a single matrix \mathbf{M} :

$$\mathbf{M} = [\mathbf{M}^1 \quad \mathbf{M}^2 \quad \dots \quad \mathbf{M}^N], \quad (8)$$

the modal scattering matrix is obtained as in (5).

Properly cascading all scattering matrices of discontinuities with those of individual homogeneous sections [12]:

$$\mathbf{D}^n = \text{Diag}\left\{\exp(-jk_{zk}^n L_n)\right\} \quad (9)$$

provides the modal scattering matrix of the entire component.

Situations of overlapping vias, such as in Fig. 1 at location C, arising from the optimization of the positions of vias are dealt with in two different ways. If vias are out of line, then individual via sections are segmented with vanishing lengths between them. If vias overlap in vertical direction, then the number N of subsections is reduced accordingly. If necessary, such overlaps are removed before the final fine-optimization run.

Finally, for fabrication and prototyping, the square via holes have to be converted to circular ones as shown in Fig. 2. Among the several conversions attempted, the one replacing the square via by a circular one whose diameter is the arithmetic mean of the side lengths of its inscribed (a_i) and circumscribed (a_o) squares [13]

$$d = 2a / (1 + 1/\sqrt{2}) \quad (10)$$

has provided the best results in all our designs.

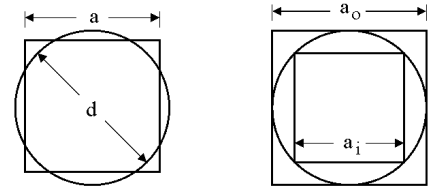


Fig. 2. Square-to-circular via conversion.

Note that this method can be extended to include microstrip ports [14], multiple input and output ports [13], T-junctions and corners [15] as well as losses within waveguide sections.

III. RESULTS

In order to demonstrate the capability of the MMT, Fig. 3 and Fig. 4 show the responses of two three-resonator filters at 21.5 GHz with a transmission zero above or below the passband, e.g. [16]. The substrate is RT/duroid 6002 with square via holes of side length 0.55 mm (circular diameter 0.644 mm [11]) and via pitch of 1.0 mm. It is obvious that

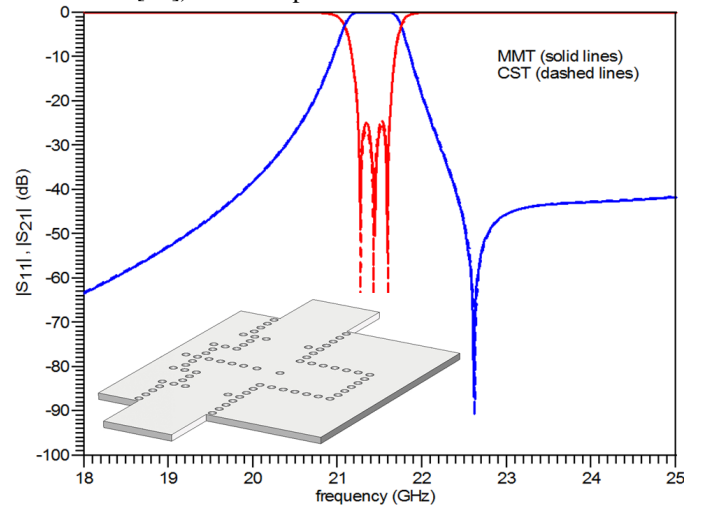


Fig. 3. SIW filter with transmission zero above passband (MMT-square vias, CST-circular vias).

the agreement is quite good despite the fact that the MMT results have been obtained with square vias while CST uses circular vias. Note that dielectric waveguide ports are used in both cases. Slight discrepancies in the return loss responses are attributed to the square-to-circular via conversion. However, since the MMT is at least an order of magnitude faster than CST, depending on code implementation, these small discrepancies are acceptable in favor of a speedy design process. Note that a loss analysis within the MMT (not shown here for lack of space), which has been shown to agree well with CST and measurements, estimates the insertion losses of the two filters in Fig. 3 and Fig. 4 at 1.6 dB and 1.8 dB, respectively.

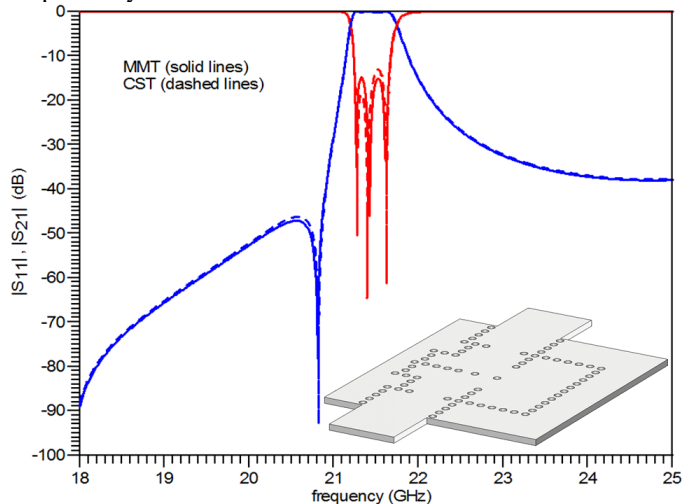


Fig. 4. SIW filter with transmission zero below passband (MMT-square vias, CST-circular vias).

VII. CONCLUSION

The MMT presents a viable option for the analysis, optimization and design of passive SIW components. A TE_{m0} mode set and an efficient square-to-circular via conversion make the MMT significantly faster than commercially available field solvers which is especially important in (fine-) optimization runs. Two filter designs demonstrate the practicality of the presented approach.

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