Project Marking Scheme

First Design (Using Smith Chart) • Stability circles • Noise circle • Input matching network • Interstage matching network • Output matching network • Transducer Power Gain	**30**
Computer Analysis (using Touchstone) • Stability circles • Noise circle • Analysis of first design using 'ideal' elements • Compare with previous results	**10**
Optimization • Design bias circuitry • Replace ideal microstrip lines with 'real' microstrip elements • Optimize using Touchstone • Print layout	**15**
Testing • Test bias voltages of completed amplifier • Measure gain and noise figure • Compare with expectations	**5**
Discussions / Conclusions	**25**
Overall Form • This includes Abstract, Introduction,..., etc.	**15**
Total	**100**